

**II. REMARKS**

The Substitute Specification, filed January 14, 2009, has been amended to correct typographical errors in Tables 13 to 18. Specifically, Tables 13 to 18 of the Substitute Specification filed January 14, 2009 includes typographical errors due to the fact that the symbol "◎" employed to designate a particular chip type was not properly transcribed and is currently represented in the Tables as a "□" in many instances (See, e.g., ¶ [0071] on page 37 of Applicant's specification as originally filed). However, according to ¶ [0071] of Applicant's disclosure as originally filed, the symbol "□" shown in Tables 13 to 18 represents another kind of chip shape. Therefore, new Tables 13 to 18 filed herewith address the typographical errors discussed above. Support for the correction is found in Tables contained in International Application No. PCT/JP2005/14691, and Japanese Patent Application No. 2004-233952, both of which have been incorporated by reference by the specification of the above-captioned U.S. Patent application.

The present amendment adds no new matter to the above-captioned application.

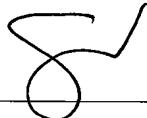
**III. CONCLUSION**

In view of the foregoing amendments and remarks, Applicants respectfully submit that the above-captioned application is in condition for allowance and a prompt notice of allowance is earnestly solicited.

The below-signed attorney for Applicant welcomes any questions.

Respectfully submitted,

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